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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

· Patent Application

Inventor(s):

Kelly, et al.

Group Art Unit:

2186

Filed:

10/30/2000

Examiner:

Thai, Tuan V.

Application No.:

09/699,947

Title:

TRANSLATED MEMORY PROTECTION APPARATUS FOR AN ADVANCED

MICROPROCESSOR

Form 1449

U.S. Patent Documents

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Examiner Initial	No.	Patent No.	Date	Patentee	Class	Sub- class	Filing Date
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Foreign Patent or Published Foreign Patent Application

Examiner	No.	Document No.	Publication Date	Country or Patent Office	Class	Sub- class	Translation	
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Other Documents

		Other Documents
Examiner Initial	No.	Author, Title, Date, Place (e.g. Journal) of Publication
Th.	R	Bedichek, R. C., Talisman: FAST AND ACCURATE MULTICOMPUTER SIMULATION, MIT 1995.
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Examiner	Tu	au V. Than Date Considered 06/14/07

Examiner: Initial citation considered. Draw line through citation if not in conformance and not considered.

Include copy of this form with next communication to applicant.